

# WL27 Series Mixed-signal LSI Test System

## Automated Test Equipment

### Overview

The WL27 system is a mixed-signal LSI test system, which can be used to test analog devices to mixed signal devices such as programmable power devices, digital consumer audio/ video signal devices and automotive devices. A real time operating system is used for the control of test sequence, and Microsoft Windows is used as operator interface. The system uses a Shibasoku original test programming language called "Tester-C", which is based on the familiar standard C programming language.



### Features

- Mixed signal device test capability
  - 128 I/O pins + 128 PPS pins
  - Parallel test of 1, 2, 4, or 8 devices
  - 4 DGS (independent device GND)
  - Ping-pong test mode for interface with two handler stations
- Highly reliable analog Test capability
  - Basic DC voltage accuracy:  $\pm 0.05\%$
  - Test modules isolated from power supply
  - Various analog modules available
  - For power devices: 30 V/30 A, 60 V/10 A, -500 to +800 V/0.2 A
  - For regular analog devices: 50 V/0.25 A, 128 V/0.25 A (40 V/2 A)
- Hi precision DSP modules
  - Audio frequency generator/digitizer
  - Video frequency generator/digitizer
- Dual high speed CPUs with Windows GUI
  - C-language based system software

### Specifications

- Per-pin Source Max. 128 pins ( $\pm 32$  V,  $\pm 32$  mA)
- Digital I/O pin Max. 128 pins ( $\pm 2$  to  $\pm 8$  V)
- Digital Test 20/40/60 MHz test rate  
Per-pin TG, 16MW vector memory

### Specifications

- Digital I/O booster amplifier output
  - Max. 32 pins ( $\pm 5$  to  $\pm 18$  V)
- DC voltmeter
  - 2 inputs
  - $\pm 32$  V max. (Direct input)
  - $\pm 512$  V max. (ATT. input)
- V/I source
  - $\pm 50$  V,  $\pm 250$  mA
  - $\pm 128$  V,  $\pm 250$  mA ( $\pm 40$  V,  $\pm 2$  A)
- Powered V/I source
  - $\pm 60$  V,  $\pm 10$  A
  - $\pm 30$  V,  $\pm 30$  A
- High voltage V/I source
  - $\pm 800$  V,  $\pm 200$  mA
- Time measurement
  - 10 nsec to 6 sec
- Resolution 100 psec
- DSP options
  - AFG: 16 bits/10 Msps
  - ADGT: 16 bits/10 Msps
  - VFG: 14 bits/240 Msps
  - VDGT: 14 bits/80 Msps
- Parallel test capability
  - 1/2/4/8 DUTs

### Target Applications

Automotive ICs, Motor Driver ICs, MCU, IPD

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